Parallelizing non-associative sequential reductions



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Abstract

A great number of algorithms can be implemented in terms of reductions, a very common functional pattern. In most cases, reductions are easily parallelizable.

There are however many important reduction applications for which a parallel implementation cannot be directly derived from their natural functional form due to non-associative reduction operators.

We present a transformation capable of deriving a parallel implementation for many reductions with non-associative operators by leveraging the associativity of matrix multiplications defined over semirings.

A trivially parallel reduction: Summation of Numbers

The formula $\sum_{0}^{n} x_n$

can be expressed functionally as the reduction

reduce(+,[1,2,3,4,5,6,7,8])

1+2+3+4+5+6+7+8

Can we parallelize Polynomial Evaluation?

The formula $\sum_{0}^{n} x_n * k^n$

Equivalent to $x_0 \odot x_1 \odot ... \odot x_n$ where $a \odot b = k * a + b$

reduce(\odot ,[1,2,3,4,5,6,7,8]) 1 \odot 2 \odot 3 \odot 4 \odot 5 \odot 6 \odot 7 \odot 8

Key insight: Matrix Multiplication is Associative

By rearranging the input [1,2,3,4,5,6,7,8] in this series of matrices

[k1] [k2] [k3] [k4] [k5] [k6] [k7] [k8] [01] [01] [01] [01] [01]

We can rewrite reduce(\odot ,[1,2,3,4,5,6,7,8]) = \times

into $reduce(\times,[[51],[52],...]) = \begin{bmatrix} a \times \\ 0 & 1 \end{bmatrix}$

x is associative*! We know how to parallelize this!

This rewrite is valid for other operators composed of semiring operations

Parallel Reduction For k = 2**- [** 2 3 **]**

Associativity

- + Associative operator: the reduction steps can be executed in any order
- Non-associative operator: the reduction steps must be executed sequentially

An operator ⊕ is associative if:

$$(a \oplus b) \oplus c = a \oplus (b \oplus c)$$

*Applicable for all Semiring Operations

- + Associative and communative operator
- * Associative operator, distributes over +
- O Neutral element for +
- 1 Neutral element for *

Why does this work? Compare

$$\begin{bmatrix} k & a \\ 0 & 1 \end{bmatrix} \times \begin{bmatrix} k & b \\ 0 & 1 \end{bmatrix} = \begin{bmatrix} k* & k & k*a+b \\ 0 & 1 \end{bmatrix}$$

το

$$a \odot b = k * a + b$$

We can use matrix multiplication to compute ①

Efficient implementation

We don't actually need to fully materialize the derived matrices, as many entries are just compile time constants

In this case for example, we just need to track the values of the first row

$$\begin{bmatrix} k & a \\ 0 & 1 \end{bmatrix} \implies \begin{bmatrix} k & a \end{bmatrix}$$